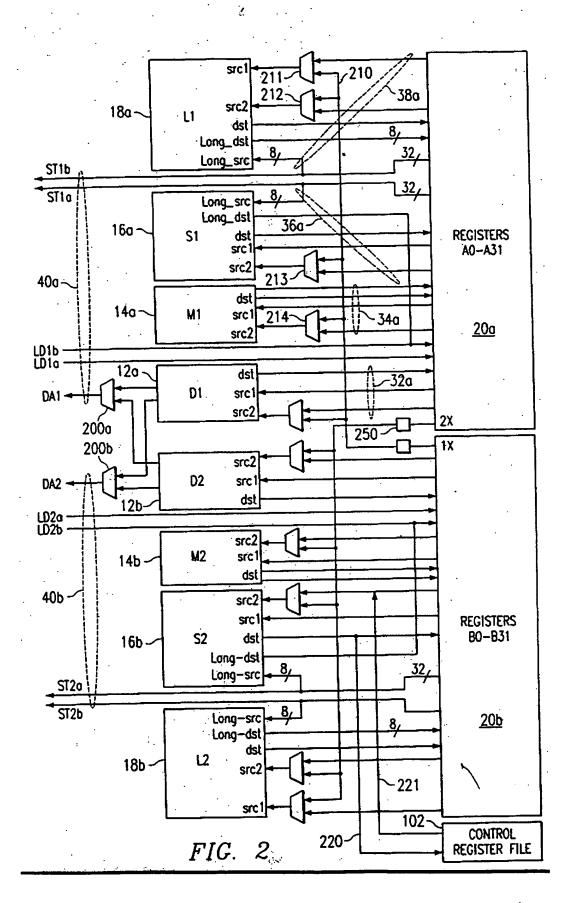


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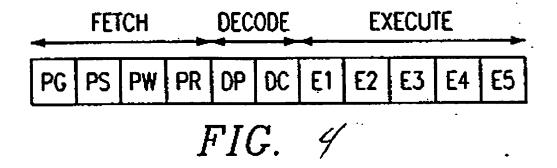
CREG Z DST SCR2 SRC1/CST X 3 5 5 5 5	
3 5 5 5	OP
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	SB.				(FIG. (3C	
てに	116	•			1	FIG	
	0000001 FIG. 3B			•	6 5 4 5 2 1 0	1 0 0 0 0 S P 1	
	g B	ഹ		1	7	оР	9
	SRC1/CST X	5			18 17 13 12	SRC1/CST	5
77	SCR2	2				SCR2	5
77 67 17	DST	. 5		DPERATIONS ON THE .D UNIT	23 22	DST	2
77 97 67 19	CREG Z	3	•	OPERATIONS C	76 96 97	CREG Z	

ر د	35	
8 7 6 4 3 2 1 0	Y LD/ST 1 1 S P FIG.	m
SET (ON THE .D UNIT) 22	UCST15	15
LOAD/STORE WITH 15-BIT OFFSET 31 29 28 27 23 22	DST/SRC	5
LOAD/STORE WITI 31 29 28 <u>27</u>	CREG Z	8

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()	FIG.	
6 4 3 2 1 0	LD/ST 0 1 S P	×
9 8 7	R	
12	MODE	4
.D UNIT 17 13	OFFSET R/UCST5	5
FSETR/CST ON THE .D UNIT 23 22 18 17	BASE R	2
Ģ	DST/SRC	5
LOAD/STORE 'BASER' + 31 29 28 27	CREG Z	3

3F	36	3H	3I	31
FIG. 3F	FIG.	FIG.	FIG.	FIG.
0P 0 0 0 0 0 S P 6	7 6 5 4 3 2 1 0 1 0 1 0 0 S P	STB OP 0 0 1 0 S P	7 6 5 4 3 2 1 0 H 1 0 1 0 5 P	7 6 5 4 3 2 1 0 0 0 1 0 0 S P
22 18 17 13 12 11 SRC2 SRC1/CST X	22 CST 16	FORMS) ON THE .S UNIT 18 17 13 12 SRC2 CSTA 5	22 DST 16	CST 21
OPERATIONS ON THE .S UNIT 31 29 28 27 23 22 CREG Z DST 5 3 2	ADDK ON THE .S UNIT 31 29 28 27 23 22 CREG Z DST 3 5	BITFIELD OPERATIONS (IMMEDIATE 31 29 28 27 23 22 CREG Z DST 3 5	MVK AND MVKH ON THE S UNIT 31 29 28 27 23 22 CREG Z DST 3 5	BCOND DISP ON THE .S UNIT 31 29 28 27 CREG Z 3



src2 src1 x Opfield	71 02 67 00 10 19511C	22 20		4 43	20 22 22 23 24 25 25 25 25 25 25 25 25 25 25 25 25 25	21	<u>- </u>		?	:	2	,			ľ
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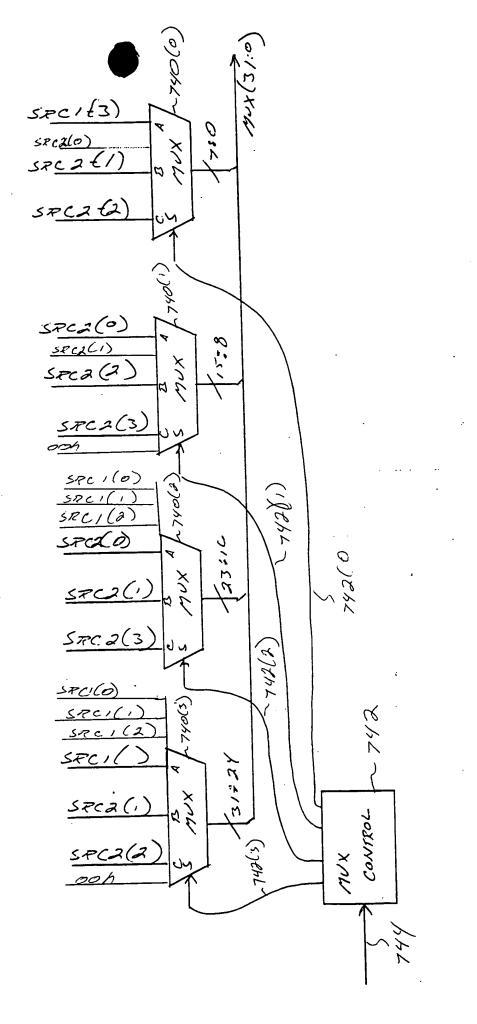
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Fig. 6J	en –	ua_1	- +	ua_0	‡	g qn	- +	ub_2	
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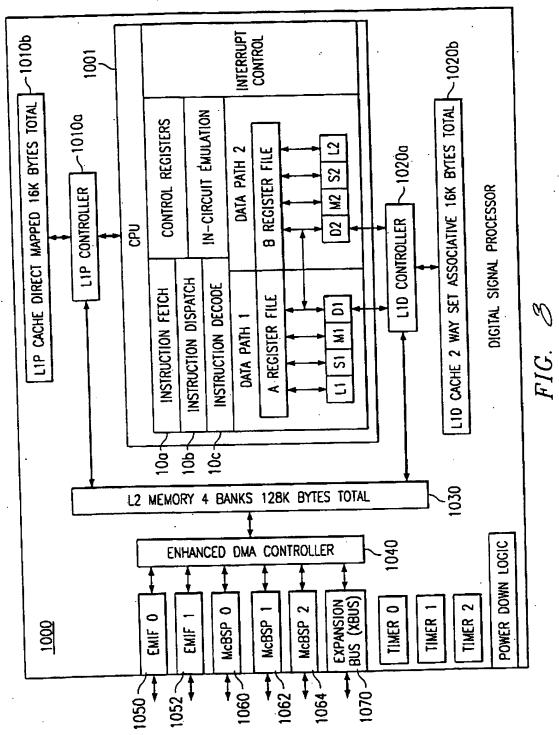
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